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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/406,788	09/28/1999	BAT-SHEVA OVADIA	023826/0141	1937
27130	7590	01/10/2006	EXAMINER	
EITAN, PEARL, LATZER & COHEN ZEDEK LLP 10 ROCKEFELLER PLAZA, SUITE 1001 NEW YORK, NY 10020			BURD, KEVIN MICHAEL	
			ART UNIT	PAPER NUMBER
			2631	

DATE MAILED: 01/10/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

**Supplemental
Notice of Allowability**

Application No.

09/406,788

Examiner

Kevin M. Burd

Applicant(s)

OVADIA ET AL.

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the amendmnet filed 10/11/2005.
2. ☒ The allowed claim(s) is/are 20-23,40-43,45-53 (renumbered to claims 1-17).
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

Allowable Subject Matter

Claims 20-23, 40-43 and 45-53 are allowed.

The following is an examiner's statement of reasons for allowance: Mujtaba (US 2002/0010895) discloses a system comprising an arithmetic logic unit (figure 9, item 82) operating in a split mode to determine trace bits for Viterbi decoding of a binary convolution code (figure 9, item 90 and paragraph 0030). Registers store the output from the arithmetic logic unit (paragraph 0032) and the trace back unit 92 generates an output bit. The trace back RAM 162 will store data from numerous stages before pushing out older bits to be replaced with current bits (paragraphs 0042 and 0043). The trace back is initiated and the trace back starts at stage 63 to push out the bit at stage 0. Mujtaba discloses the input bit is shifted into the least significant bit of the shift register while the most significant bit is shifted out of the register (paragraph 0004). However, Mujtaba does not disclose the specifics of the trace back register stated in claims 20-23 or the step of tracing back through the memory cells as stated in claims 40-43.

Meyer (US 5,802,115) discloses a method of tracing back states of convolution codes using Viterbi decoding (column 1, lines 6-9 and column 6, lines 32-38). The trace bits are stored in memory 20'. A decision is modified at each clock cycle to trace back a decision and the decision is stored in a memory where the write operation is achieved every k cycles (column 5, line 60 to column 6, line 5. K is two or more (column 5, lines 66 to column 6, line 1). All of the bits are stored. Meyer does not disclose tracing back

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the states comprise which clock cycles, the specific bit generation and bit storage take place in, as stated in claims 45-53 take place.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin M. Burd whose telephone number is (571) 272-3008. The examiner can normally be reached on Monday - Friday 9 am - 5 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammad Ghayour can be reached on (571) 272-3021. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kevin M. Burd
1/9/2006


KEVIN BURD
PRIMARY EXAMINER